

Atty. Docket No. DE9-1999-0050US1  
(590.018)

**REMARKS**

Applicants and the undersigned are most grateful for the time and effort accorded the instant application by the Examiner. The Office is respectfully requested to reconsider the rejections presented in the outstanding Office Action in light of the following remarks.

Claims 1 - 4 were pending in the instant application at the time of the outstanding Office Action. Of these claims, Claims 1 and 2 are independent claims; the remaining claims are dependent claims.

The Abstract has been objected to on formal grounds. Applicant has provided a revised Abstract, which Applicant believes is no more than 149 words in length. Accordingly, it is believed this objection has been obviated.

The drawings have also been objected to on formal grounds. Attached hereto is an annotated sheet showing proposed changes to Figs. 1 and 2 of the drawings. In Figure 1, previously omitted elements 27a, 29a, 27c, and 27 have been added. In Figure 2, the legend Prior Art has been added. Upon approval of these proposed changes, a replacement drawing sheet will be submitted.

Claim 2 has been amended to address the formal issue raised. Applicant has not changed the scope of the claim by this amendment.

All claims have been rejected under 35 U.S.C. § 103(a). Claim 1 stand rejected under 35 U.S.C. 103(a) over Upton in view of Figure 2 and statements made in the

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specification (pages 1-3) and further in view of "Official Notice". Claims 2-4 also stand rejected under 35 U.S.C. 103(a) over Upton in view of Figure 2 and statements made in the specification (pages 1-3). Reconsideration and withdrawal of the present rejections are hereby respectfully requested.

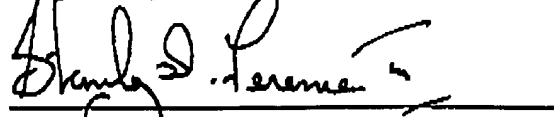
Upton as best understood appears to be directed to reconfigurable processors, that is, a processor whose architecture can be interchanged among classical Von Neumann architecture and other architectures such as parallel, systolic, pipeline, etc., very quickly. (Col. 1, lines 12; Col. 3, lines 9-11) Each type of architecture appears to require different function sequences (120-1 through 120-N) which are stored in macro array library 204 with programmable read only memory modules (PROMs) 210, 212, 214 and 216 which store function sequences 120-1 through 120-N. Data link 300 appears to be used for "reconfiguring" the PROMs, i.e., making certain PROMs available and thereby making certain function sequences available, depending upon which type of architecture is being used. Upton, however, does re-program the PROMs with different function sequences. (QUERY: Can PROMs be re-programmed? See Page 3, lines 1-2; "a new PROM has to be used, as the used one can not be rewritten.")

In view of the foregoing, it is respectfully submitted that Claims 1-4 fully distinguish over the applied art and are thus in condition for allowance. Notice to that effect is earnestly solicited. In the unlikely event the Office does not agree the

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application is in condition for allowance, Applicants respectfully request an interview  
with the Examiner prior to the next Office Action in this case.

Respectfully submitted,



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